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Application Number	10/724,138
Filing Date	12/1/2003
First Named Inventor	Buerke et al.
Title	Low Resistance Barrier for a Microelectronic Component and Method for Fabricating the Same
Art Unit	2811
Examiner Name	Hung K. Vu
Attorney Docket Number	INF-118

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 Applicant/Inventor Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).**SIGNATURE of Applicant or Assignee of Record**

Name	Ms. Post	Ms. Girstenbrau	Date: 13.12.2004
Signature	i. v. Post	i. v. Girstenbrau	
Title and Company	both authorized Patent administrator, Infineon Technologies AG	Telephone	+49-89-234-28413

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